

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of:
MORIN ET AL.

Serial No. 10/701,165

Confirmation No. 5541

Filing Date: November 4, 2003

For: SEMICONDUCTOR DEVICE WITH MOS
TRANSISTORS WITH AN ETCH-STOP LAYER)
HAVING AN IMPROVED RESIDUAL STRESS)
LEVEL AND METHOD FOR FABRICATING)
SUCH A SEMICONDUCTOR DEVICE)

Examiner: S. GEBREMARIAM

Art Unit: 2811

AMENDMENT AFTER FINAL

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Official Action of August 24,
2005, please enter the amendments and consider the remarks set
out below.

Entered w/ RCE
(MB)
2/23/06

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